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AMENDMENT TRANSMITTAL LETTER			Docket No. 03226/035001; P5030
Application No. 09/994,479-Conf. #7240	Filing Date November 27, 2001	Examiner K. S. Kim	Art Unit 2111

Applicant(s): David M. Pini et al.

Invention: Method of Processing Signed Loads with the Same Latency as Unsigned Loads in a High-Frequency Processor

**TO THE COMMISSIONER FOR PATENTS**

Transmitted herewith is an amendment in the above-identified application.

The fee has been calculated and is transmitted as shown below.

CLAIMS AS AMENDED					
	Claims Remaining After Amendment	Highest Number Previously Paid	Number Extra Claims Present	Rate	
Total Claims	9	- 20 =		x	
Independent Claims	4	- 4 =		x	
Multiple Dependent Claims (check if applicable) <input type="checkbox"/>					
Other fee (please specify):					
<b>TOTAL ADDITIONAL FEE FOR THIS AMENDMENT:</b>					0.00

- ☒ Large Entity ☐ Small Entity
- ☒ No additional fee is required for this amendment.
- ☐ Please charge Deposit Account No. \_\_\_\_\_ in the amount of \$ \_\_\_\_\_.  
A duplicate copy of this sheet is enclosed.
- ☐ A check in the amount of \$ \_\_\_\_\_ to cover the filing fee is enclosed.
- ☐ Payment by credit card. Form PTO-2038 is attached.
- ☒ The Director is hereby authorized to charge and credit Deposit Account No. 50-0591  
as described below. A duplicate copy of this sheet is enclosed.
- ☒ Credit any overpayment.
- ☒ Charge any additional filing or application processing fees required under 37 CFR 1.16 and 1.17.

*W. K. Osha* REG. NO. 33,986  
51,048  
For Jonathan P. Osha  
Attorney Reg. No.: 33,986

Dated: March 18, 2005

OSHA & MAY L.L.P.  
1221 McKinney St., Suite 2800  
Houston, Texas 77010  
(713) 228-8600

I hereby certify that this correspondence is being deposited with the U.S. Postal Service as Express Mail, Airbill No. EV562273192US, in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on the date shown below.

Dated: March 18, 2005

Signature: *Yuki Tsukuda* (Yuki Tsukuda)



Docket No.: 03226/035001; P5030  
(PATENT)

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

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In re Patent Application of:  
David M. Pini et al.

Confirmation No.: 7240

Application No.: 09/994,479

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Examiner: K. S. Kim

For: Method of Processing Signed Loads with the  
Same Latency as Unsigned Loads in a High-  
Frequency Processor

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**REPLY UNDER 37 C.F.R. § 1.116**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

In response to the final Office Action dated January 18, 2005, please reconsider the present application in view of the following.